

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	((multi adj chip adj module) and (spare adj processor)).clm.	US-PGPUB	OR	ON	2007/04/06 17:56
L2	1	((first spare) and unavailable and (second adj spare adj processor)).clm.	US-PGPUB	OR	ON	2007/04/06 17:56
L3	0	((simultaneously adj utilizing) and (first adj spare adj processor) and (second adj spare adj processor)).clm.	US-PGPUB	OR	ON	2007/04/06 17:58
L4	0	((first adj spare adj processor) and (second adj spare adj processor)).clm.	US-PGPUB	OR	ON	2007/04/06 17:58

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S93	811	((spare redundant) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:49
S94	2142	((((back adj up) backup spare (stand adj by) alternate redundant standby) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:06
S95	62723	((dual adj core) (multi adj (processor core)) multiprocessor multicore)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:01
S96	123	S94 same S95	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:08
S97	5728	((((back adj up) backup spare (stand adj by) alternate redundant standby slave secondary failover) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:38
S98	485	S97 same S95	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:28
S99	19	(dual adj core) and S97	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:29

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S10 0	17	(multi adj core) and S97	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:29
S10 1	24	(chip die) same ((spare redundant) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 16:51
S10 2	24	(core die) same ((spare redundant) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 15:50
S10 3	134	(dual adj processor) and spare	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 16:52
S10 4	7	(dual adj processor) same spare	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 16:54
S10 5	5	hypervisor and ((spare standby) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/10 16:55
S10 6	91	firmware and ((spare standby) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:37

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S10 7	540	714/13.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 13:15
S10 8	1782	714/10-13.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 15:08
S10 9	5731	((back adj up) backup spare (stand adj by) alternate redundant standby slave secondary failover) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:38
S11 0	276	S108 and S109	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:56
S11 1	93	S107 and (die chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:52
S11 2	504	vaidyanathan.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:53
S11 3	4	(vaidyanathan with basu).in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 14:38

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S11 4	752	S109 and (firmware hypervisor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:56
S11 5	124	S109 same (firmware hypervisor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 10:57
S11 6	10	S108 and S115	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 11:00
S11 7	1	"7017074" and firmware	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 11:01
S11 8	188	open adj firmware	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 11:10
S11 9	46	(open adj firmware) and (IEEE adj "1275")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 11:32
S12 0	188	(open adj firmware)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 11:24

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S12 1	5731	((back adj up) backup spare (stand adj by) alternate redundant standby slave secondary failover) adj processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 11:24
S12 2	9	(open adj firmware) and S121	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 11:25
S12 6	54	("20020133751" "20020152419" "4445174" "4590554" "4691280" "5051887" "5155809" "5206939" "5255270" "5488716" "5491787" "5734818" "5737605" "5742792" "5758052" "5764882" "5802265" "5826081" "5893140" "5901281" "5926619" "5938765" "5951695" "5978565" "5987621" "6003123" "6065053" "6073218" "6108300" "6112286" "6145101" "6182089" "6192408" "6199179" "6223204" "6230200" "6240501" "6260120" "6269390" "6275953" "6289369" "6292905" "6314501" "6314515" "6324581" "6327670" "6363464" "6381682" "6430667" "6594735" "6604182" "6708285" "6728896" "6813522").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/11/12 12:30
S12 7	350	(plurality with processors) with (backup standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 13:16
S12 8	110	(plurality adj3 processors) with (backup standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 13:19

EAST Search History

S12 9	4	(plurality adj3 processors adj modules) with (backup standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 13:22
S13 0	14	(plurality adj3 processors adj modules) same (backup standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 13:24
S13 1	1	(plurality adj3 processors adj modules) same (standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 13:49
S13 2	0	"5485604".pn. and availability	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 14:38
S13 3	0	"5485604".pn. and available	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 14:39
S13 4	884420	reliability	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 14:39
S13 5	2	"5485604".pn. and reliab\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:19

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S13 6	188	open adj firmware	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:59
S13 7	2	(open adj firmware) same spare	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:06
S13 8	123	open adj firmware and multiprocessor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:00
S13 9	123	(open adj firmware) and multiprocessor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:04
S14 0	1	"10/621945"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 19:48
S14 1	1	S140 and firmware	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:04
S14 2	1	(open adj firmware) with failure	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:06

EAST Search History

S14 3	8	((open adj firmware) hypervisor) with detect\$3 with failure	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 18:11
S14 4	1	"10/621945" and (medium product)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 19:48
S14 5	568	714/13.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/04 16:20
S15 2	6	"6694450".pn. "5892946".pn. "5878205".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 18:01
S15 3	90	714/10-13.ccls. and @pd>"20061112"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 15:13
S15 5	702	714/11.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 15:10
S15 6	66858	((dual adj core) (multi adj (processor core)) multiprocessor multicore)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 15:15

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S15 7	4141	@pd>"20061112" and S156	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 15:17
S15 9	186	"714"/\$.ccls. and S157	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 15:17
S16 5	1462	"714"/\$.ccls. and ((backup standby alternate) adj3 (processor unit device))	US-PGPUB; USPAT	OR	ON	2007/04/04 15:23
S16 6	1507	"714"/\$.ccls. and ((backup standby alternate failover) adj3 (processor unit device))	US-PGPUB; USPAT	OR	ON	2007/04/04 15:23
S16 7	266	714/11.ccls. and (backup spare)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/04 17:04
S16 8	1133	(load adj3 sharing) and (backup spare)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/04 17:05
S16 9	350	"714"/\$.ccls. and (redundant adj3 processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 17:43
S17 0	266	"714"/\$.ccls. and (logical adj partition)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 17:44

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S17 1	1	10/663844	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 18:02
S17 2	2	loadshar\$3 same backup	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/04 18:02
S17 3	620	((dual adj (core processor unit)) (multi adj (processor core unit)) multiprocessor multicore) same (backup spare standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 10:06